

C  
VT  
#2  
3/26/03



Appl. No. 09/322,259

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : John Terrance Reagan, et al. ;  
Appl. No. : 09/322,259 ; Grp./Art Unit : 2654  
Filed : 28 May 1999 ; Examiner: M. Opsasnick  
Title : Reducing Artifact Generation In  
A Vocoder ;  
  
Docket No. : PHA51214 (New) ;  
VLSI-3410 (Old) ;

Assistant Commissioner for Patents  
Washington DC 20231

**RECEIVED**  
FEB 21 2003  
**Technology Center 2600**

**RESPONSE and AMENDMENT**

Sir:

This is in response to the Office Action of 09 August 2002.

IN THE CLAIMS

Please amend Claims 1, 5, 9, and 13 as follows.

Sub  
D.

C1

- 1 1. (Amended) A computer implemented method of reducing sinusoidal artifact
- 2 generation in a vocoder, said computer implemented method comprising the
- 3 steps of:
- 4 a) receiving a determined input energy threshold value below which a
- 5 suspected noise-inducing codebook excitation vector is expected to be generated
- 6 by said vocoder; and
- 7 b) provided an input signal is received having an energy value lower than
- 8 said input energy threshold value, using a selection process to such that said
- 9 suspected noise-inducing codebook excitation vector from is not continuously
- 10 generated;
- 11 wherein the input signal comprises a plurality of subframes, the subframes